

A-SSCC 2024 At-a-glance including IC Circuits on November 17
November 18-21, 2024 Hiroshima International Conference Center, Hiroshima, Japan

2-Sep-24

Monday, November 18, 2024				Phoenix Hall	Himawari	Dahlia-1	Dahlia-2	Cosmos-1	Cosmos-2	Ran-1	Sakura
9:00	10:20	1:20			Tutorial I						
10:20	10:40	0:20			Break						
10:40	12:00	1:20			Tutorial II						
12:00	13:20	1:20	Lunch time		Lunch time						
13:20	14:40	1:20			Tutorial III						
14:40	15:00	0:20	Break		Break						
15:00	16:20	1:20			Tutorial IV						
16:20	16:40	0:20	Break		Break						
16:40	18:00	1:20			Convergence Workshop			LEAP			
18:00	19:00	1:00			Welcome Reception						
19:00	20:00	1:00			TPC Meeting						
Tuesday, November 19, 2024				Phoenix Hall	Himawari	Dahlia-1	Dahlia-2	Cosmos-1	Cosmos-2	Ran-1	Sakura
8:30	8:40	0:10		Opening Ceremony							
8:40	9:20	0:40		Plenary Pannel							
9:20	10:05	0:45		Plenary Talk 1							
10:05	10:50	0:45		Plenary Talk 2							
10:50	11:10	0:20	Break								
11:10	12:50	1:40			Industry Forum	IT Vision in Asia					
12:50	14:10	1:20	Lunch time								
14:10	15:50	1:40			Session 1 (A1L-B) Integrated IndustryDesign Techniques Chr: Takayuki Miyazaki , Saki Hatta	Session 2 (A1L-A) Hybrid Converters Chr: Po-Hung Chen, Wanyuan Qu	Session 3 (A1L-C) Efficient AI and DSP Processors Chr: Dongsuk Jeon, Koyo Nitta	Session 4 (A1L-D) Emerging Wireless and Battery-Free Systems Chr: Hsiao-Chin Chen, Joonsung Bae			14:10-18:10 SDC Exhibition/FPDA Demo
15:50	16:10	0:20	Break								
16:10	17:50	1:40		Session 8 (A2L-D) SRAM-Based Computing-in- Memory Chr: Tae-Hyung Tony Kim, Ken Takeuchi	Session 5 (A2L-A) DC-DC Converters Chr: Makoto Takamiya, Hiroshi Fuketa			Session 6 (A2L-B) RF/mm-Wave Building Blocks Chr: Kuang-Wei Cheng, Hao Gao	Session 7 (A2L-C) FPGA Implementation for Emerging Applications Chr: Youngjoo Lee, Yoshiki Yamaguchi	Sponsor session	
17:50	18:10	0:20	Break								
18:10	19:40	1:30			Social Event						

Wednesday, November 20, 2024				Phoenix Hall	Himawari	Dahlia-1	Dahlia-2	Cosmos-1	Cosmos-2	Ran-1	Sakura
9:00	9:45	0:45		Plenary Talk 3							
9:45	10:30	0:45		Plenary Talk 4							
10:30	10:50	0:20	Break								
10:50	12:30	1:40			Session 9 (B1L-A) High-Speed Nyquist ADCs Chr: Seunghyun Oh, Hsin-Shu Chen	Session 10 (B1L-B) Phased Arrays Chr: Kenichi Okada, Jaehyouk Choi	Session 11 (B1L-C) AI Accelerators Chr: Kyuho Lee, Kun-Chin Chen	Session 12 (B1L-D) Clock and Data Recovery Circuits Chr: Jung-Hoon Chun, Takashi Toi			
12:30	14:00	1:30	Lunch time								
14:00	15:40	1:40			ACE Program	Session 13 (B2L-C) Low-Jitter PLLs and Low-Phase-Noise Oscillators Chr: Giovanni Mangraviti, Yusuke Wachi	Session 14 (B2L-B) Amplifiers and Power Management Chr: Pieter Harpe, Paras Garg	Session 15 (B2L-D) Energy-Efficient Circuit Level Techniques Chr: Mototsugu Hamada, Massimo Alioto			14:00-17:40 Industry Demo
15:40	16:00	0:20	Break								
16:00	17:40	1:40			Panel Discussion Chr: Baoyong Chi, Chien-Nan Kuo	Session 16 (B3L-C) Millimeter-Wave Transceivers Chr: Baoyong Chi, Chien-Nan Kuo	Session 17 (B3L-B) Biosignal and Biomedical Applications Chr: Kea-Tiong Tang, Zhao Bo		WIC & YP Mentoring		
17:40	18:00	0:20	Break								
18:00	19:30	1:30	Banquet at ANA Crowne Plaza Hiroshima								
Thursday, November 21, 2024				Phoenix Hall	Himawari	Dahlia-1	Dahlia-2	Cosmos-1	Cosmos-2	Ran-1	Sakura
9:00	10:40	1:40			Session 18 (C1L-C) Temperature Resilient Analog Circuits Chr: Min-Jae Seo, Taekwang Jang	Session 19 (C1L-B) Continuous-time ADCs Chr: Takashi Oshima, Lu Jie	Session 20 (C1L-A) Innovative Computing and Circuit Design Chr: Takuji Miki, Wanyeong Jung	Session 21 (C1L-D) Application-Specific Processors Chr: Shuou Nomura, Yu-Guang Chen			
10:40	11:00	0:20	Break								
11:00	12:40	1:40			RISE	Start-up Company Forum					
12:40	14:00	1:20	Lunch time								
14:00	15:40	1:40			Session 22 (C2L-C) Advanced Wireline Transceivers Chr: Peng Liu, Pen-Jui Peng	Session 23 (C2L-B) Energy-Efficient and High-Resolution ADCs Chr: Chi-Hang Chan, Hyungil Chae	Session 24 (C2L-A) Advanced Sensor and Imaging Technologies Chr: Yongfu Li, Chihiro Okada	Session 25 (C2L-D) High Density Computing-In-Memory Chr: Chixiao Chen, Anh Tuan Do			
15:40	16:00	0:20	Break								
16:00	17:40	1:40			Session 26 (C3L-C) Clocking and Memory Interfaces Chr: Tetsuya Iizuka, Masum Hossain	Session 27 (C3L-B) Security/Signal Processing Systems Chr: Weiwei Shan, Daisuke Mizoguchi		Session 28 (C3L-D) Advanced Memory Technology Chr: Akihiko Kanda, Koichi Kinoshita	Session 29 (C3L-A) Advanced Transceivers Chr: Mustafijur Rahman, Dixian Zhao		
17:40	18:00	0:20	Break								
18:00	19:30	1:30	Farewell Party				Farewell Party				